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Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
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EFS ID: 61867  
Application ID: 10091647  
Title of Invention: Reset facility for redundant processor using a fivre channel loop  
First Named Inventor: Aedan Coffey  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2002-03-05  
Effective Receipt Date: 2004-06-02  
Submission Type: Information Disclosure Statement  
Filing Type:  
Confirmation number: 9151  
Attorney Docket Number: ERLG.P-032




Total Fees Authorized:

Digital Certificate Holder: cn=Carl Oppedahl,ou=Registered Attorneys,ou=Patent and Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US  
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# TRANSMITTAL

Electronic Version v1.1  
Stylesheet Version v1.1.0

Title of Invention	Reset facility for redundant processor using a fivre channel loop									
Application Number: 10/091647 										
Date: 2002-03-05										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Carl Oppedahl Registered Number: 32746</td><td>/s/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Carl Oppedahl Registered Number: 32746	/s/	Attorney		
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	us-ids.xsl									
<p>Comments</p> <p>No fees are believed due with this submission as we have not received an action on</p>										


the merits. However, the Commisioner is authorized to debit any fees which may be due or credit any overpayments to Deposit Account No. 15-0610.



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Reset facility for redundant processor using a five channel loop																																																																																																		
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